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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): HARADA et al.

Application No.: 10/814,804

Filed: April 1, 2004

Group Art Unit: 1775

Title: MULTI-LAYER PRINTED CIRCUIT  
BOARD AND METHOD FOR  
MANUFACTURING THE SAME

Examiner: Lam, Cathy Fong Fong

Attorney Docket No.: 01-628

Commissioner for Patents  
Alexandria, VA 22314

April 28, 2005

**RESPONSE TO RESTRICTION REQUIREMENT**

Sir:

In response to a Restriction Requirement mailed on April 28, 2005 in connection with the above application, Applicants hereby elect Invention I (claims 1-7, drawn to a multilayer printed circuit board) without traverse.

Examination of the present application in view of the above election is respectfully requested.

Please charge any necessary fees to Deposit Account 50-1147.

Respectfully submitted,

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